

Fig. 1

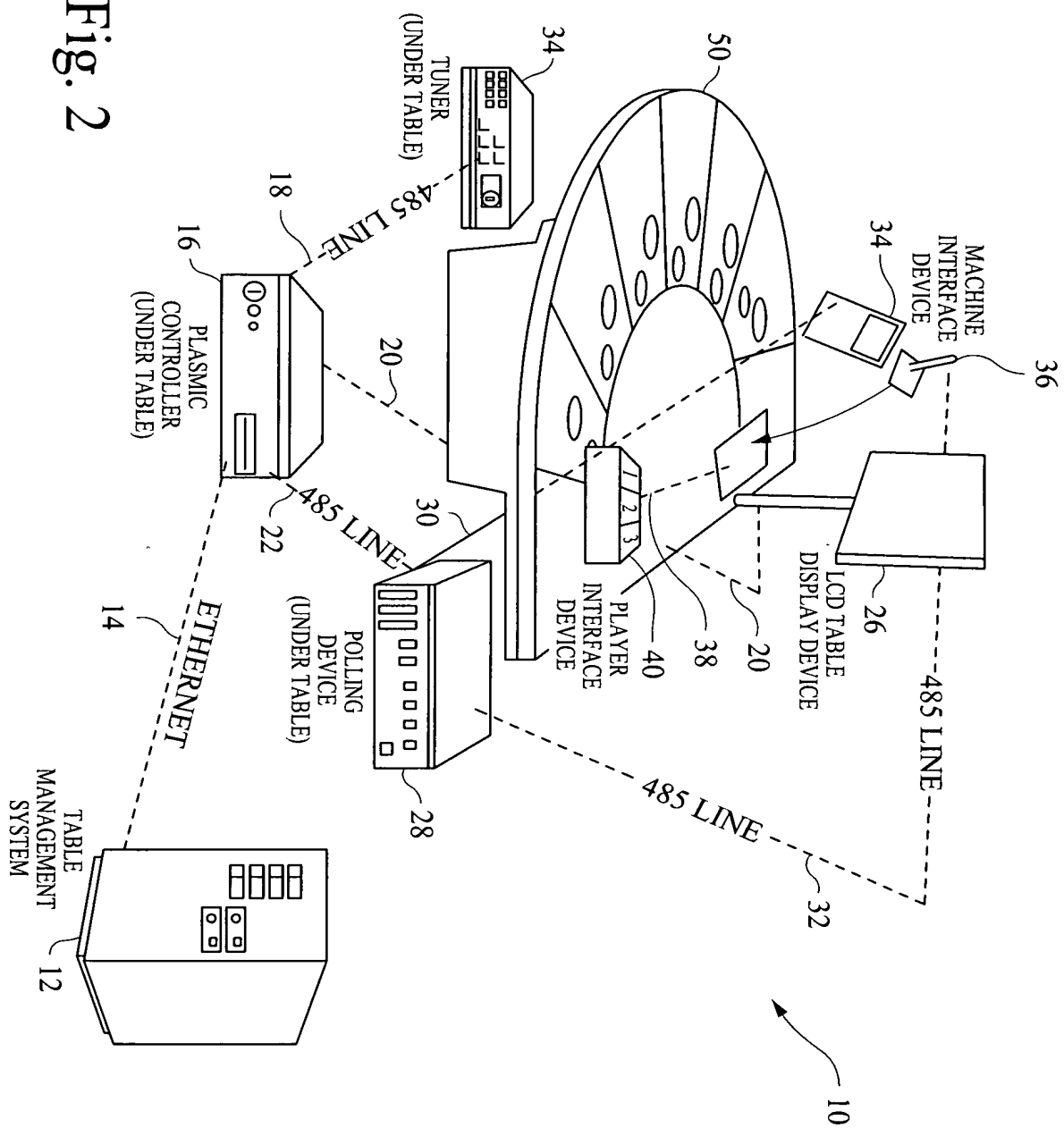


Fig. 2

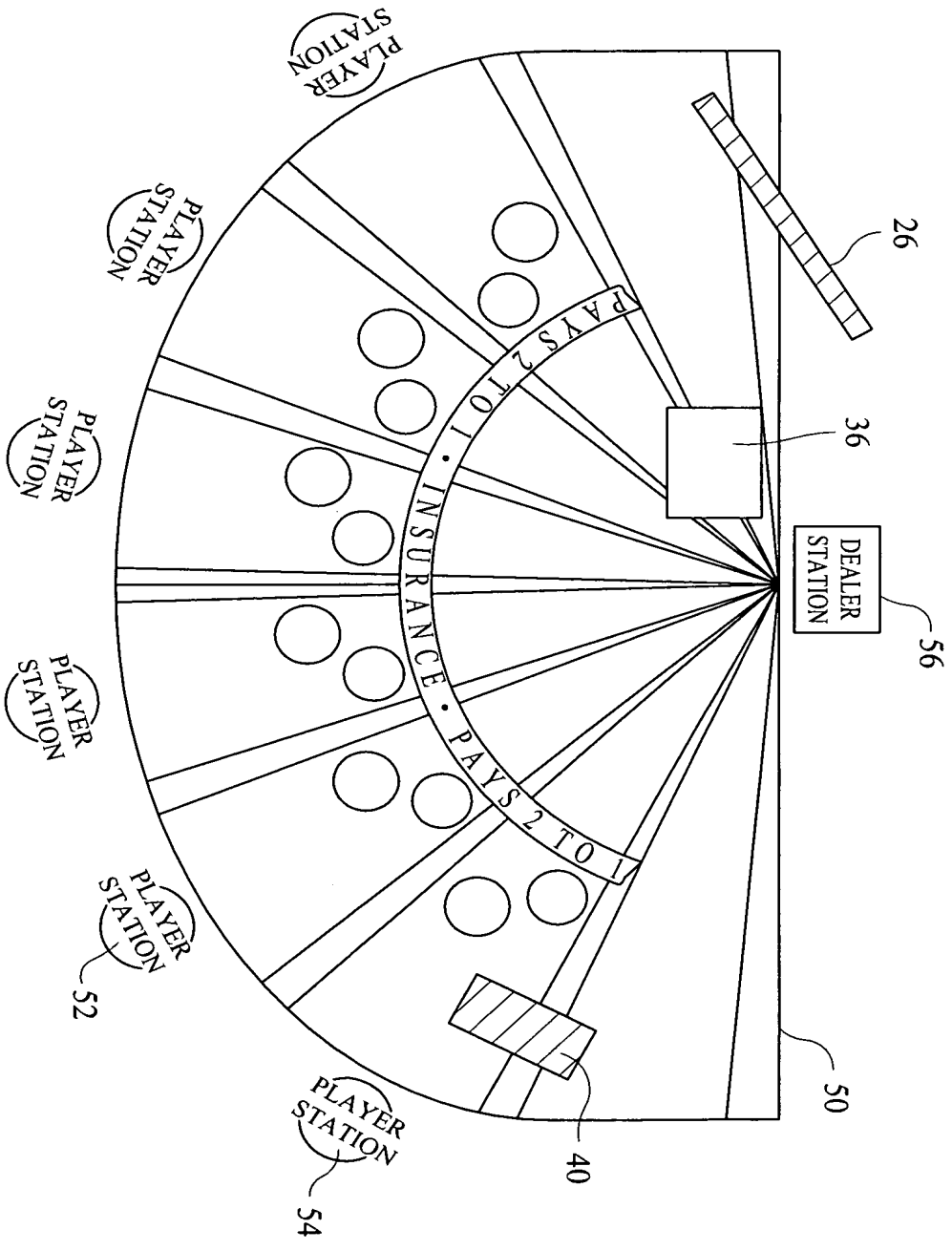
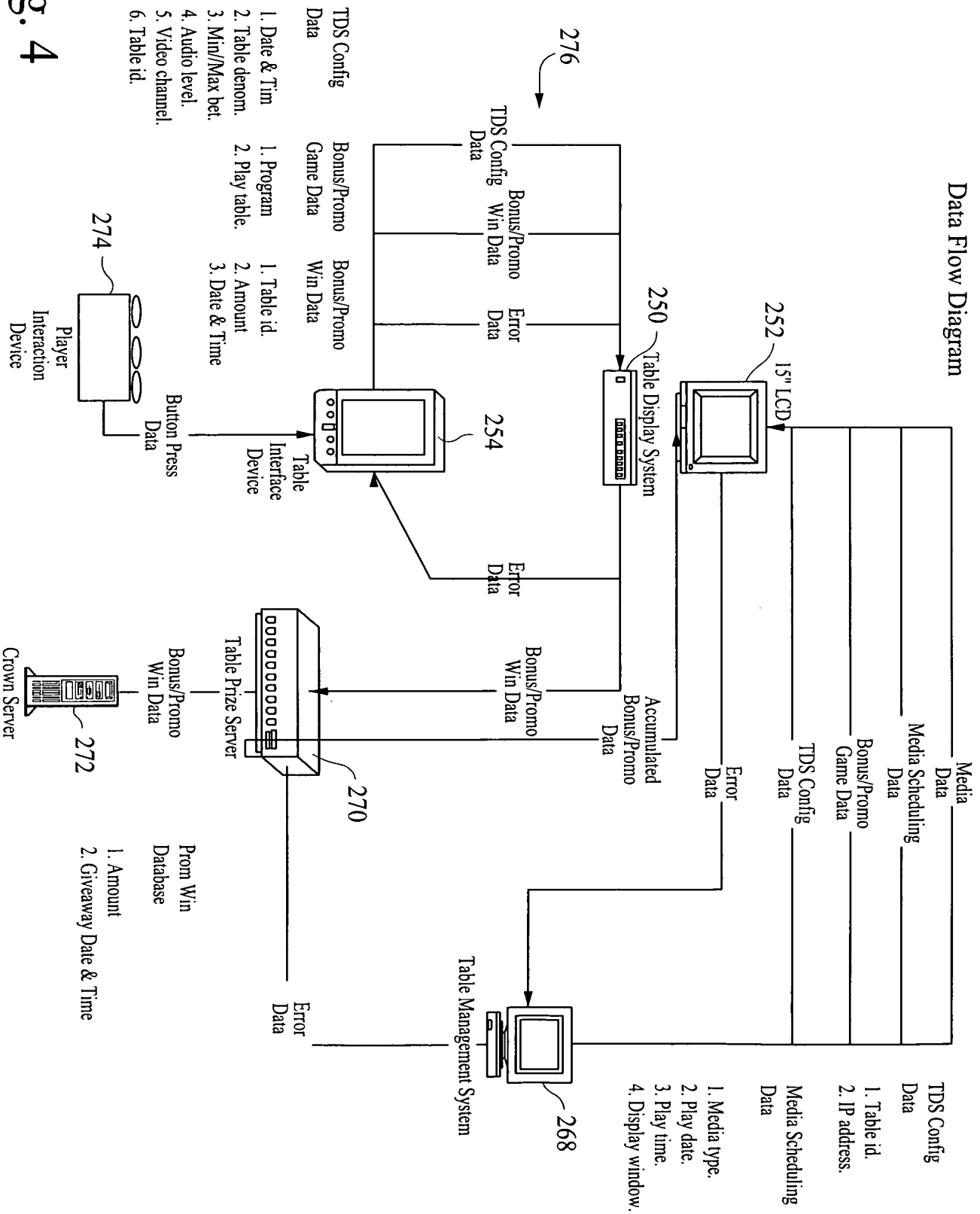


Fig. 3

Fig. 4



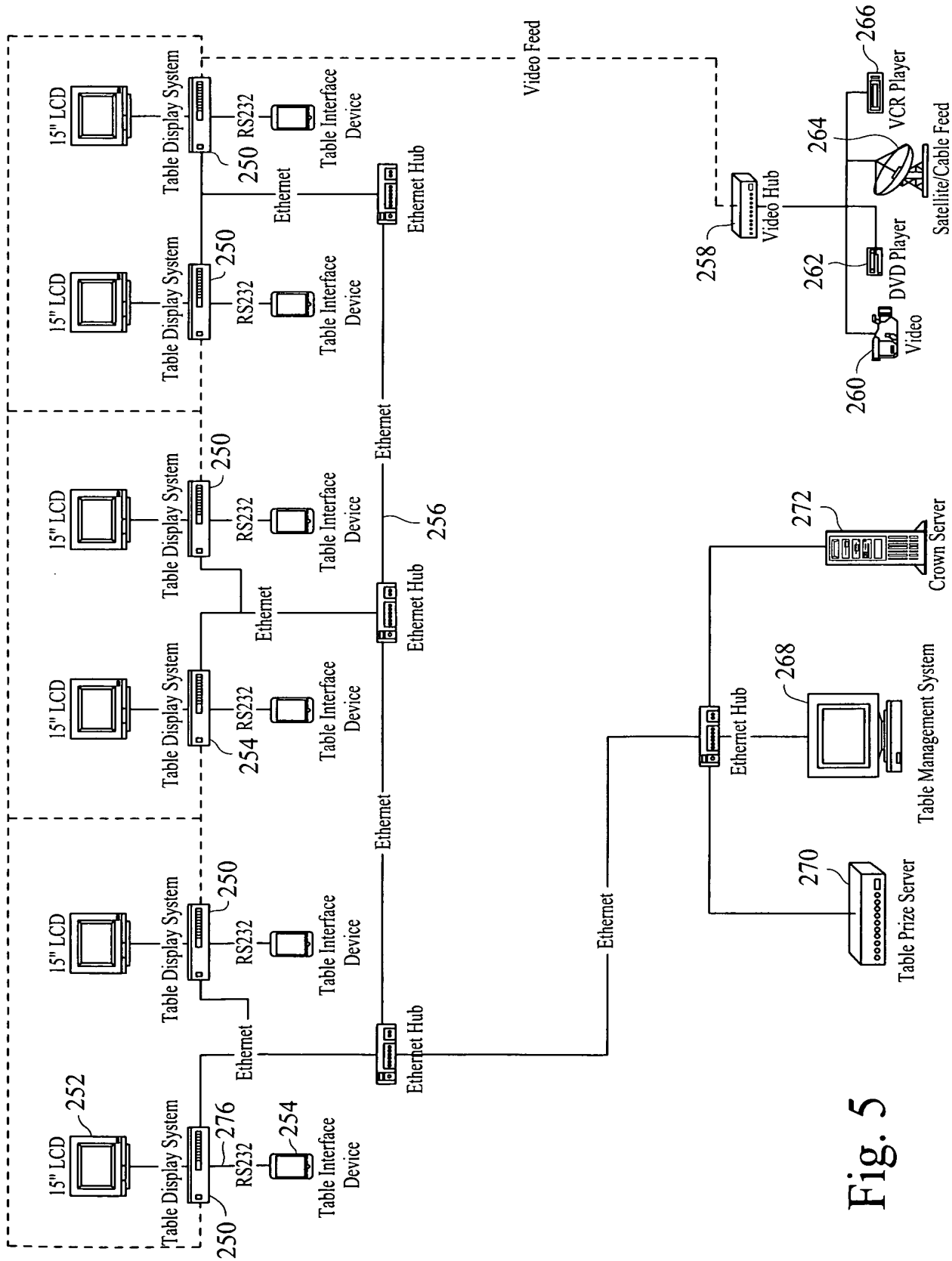


Fig. 5

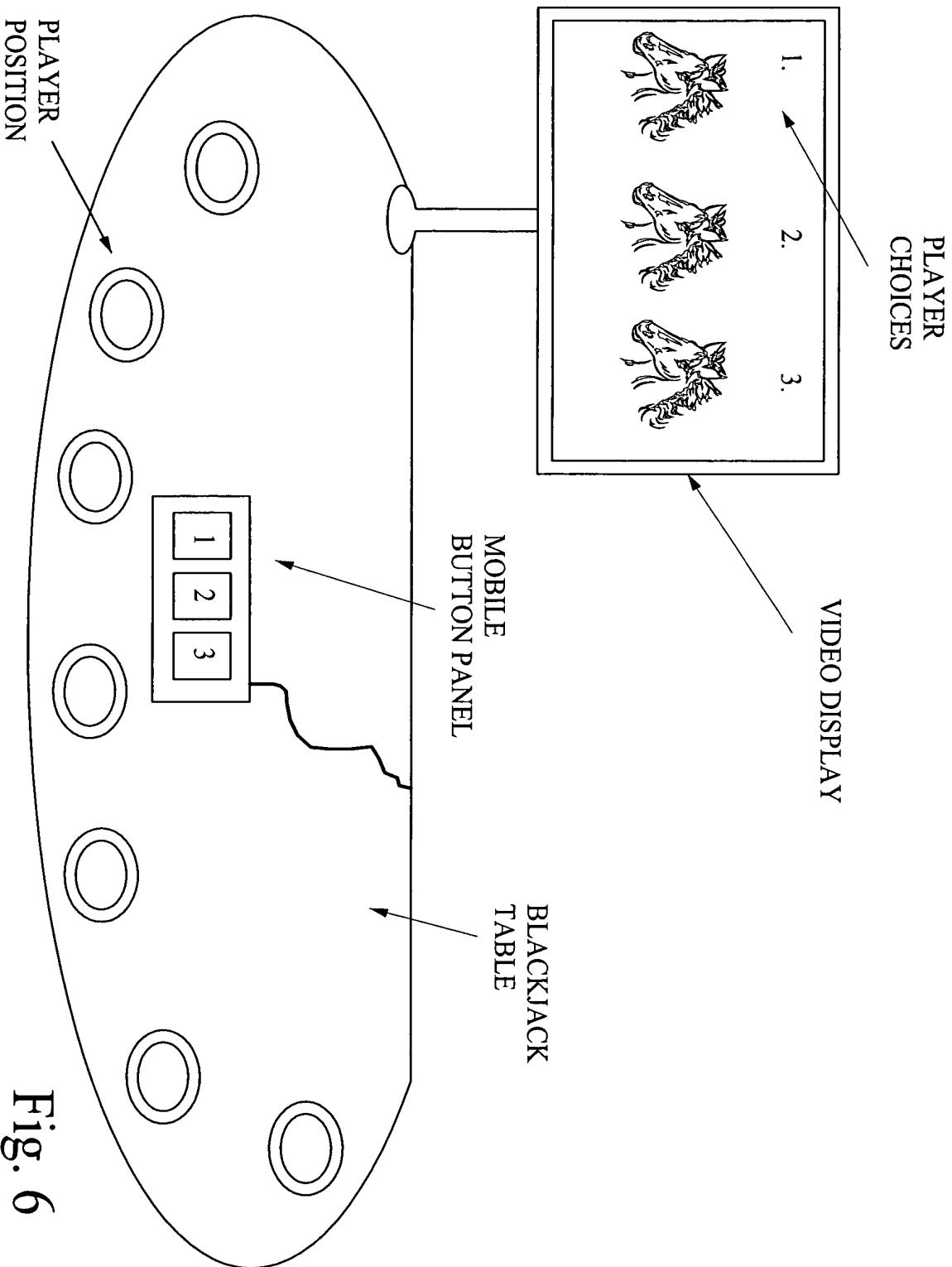


Fig. 6

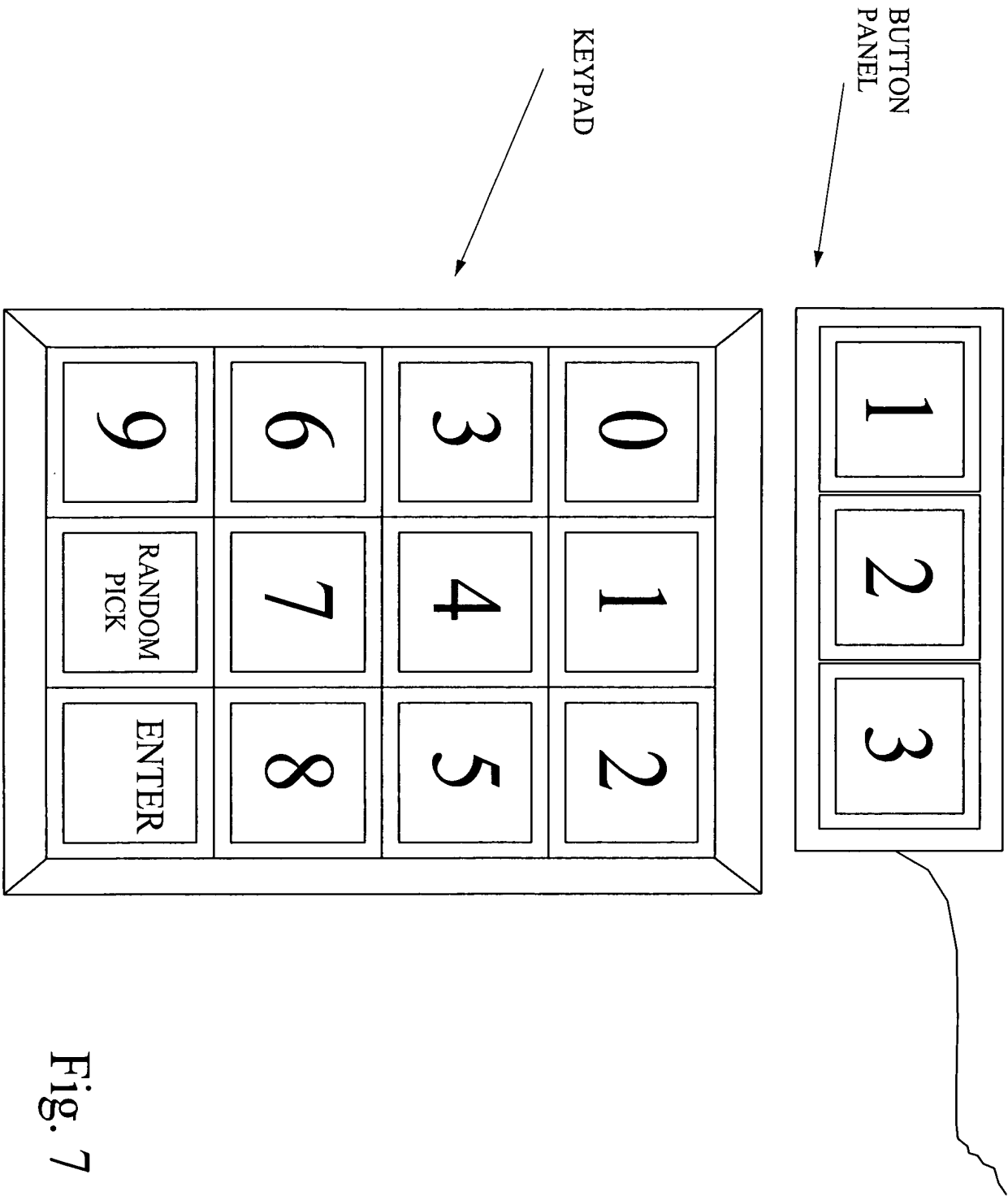


Fig. 7

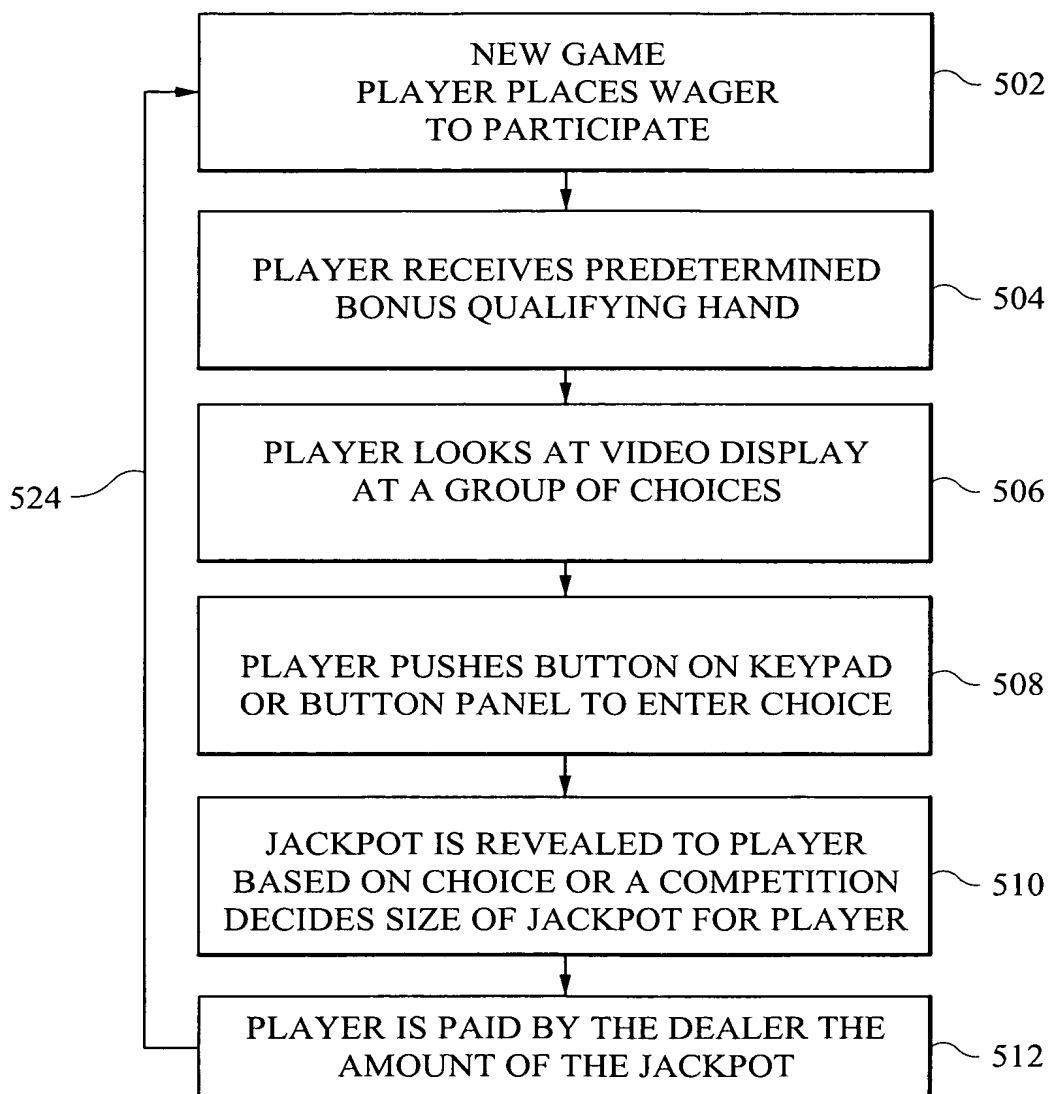


Fig. 8

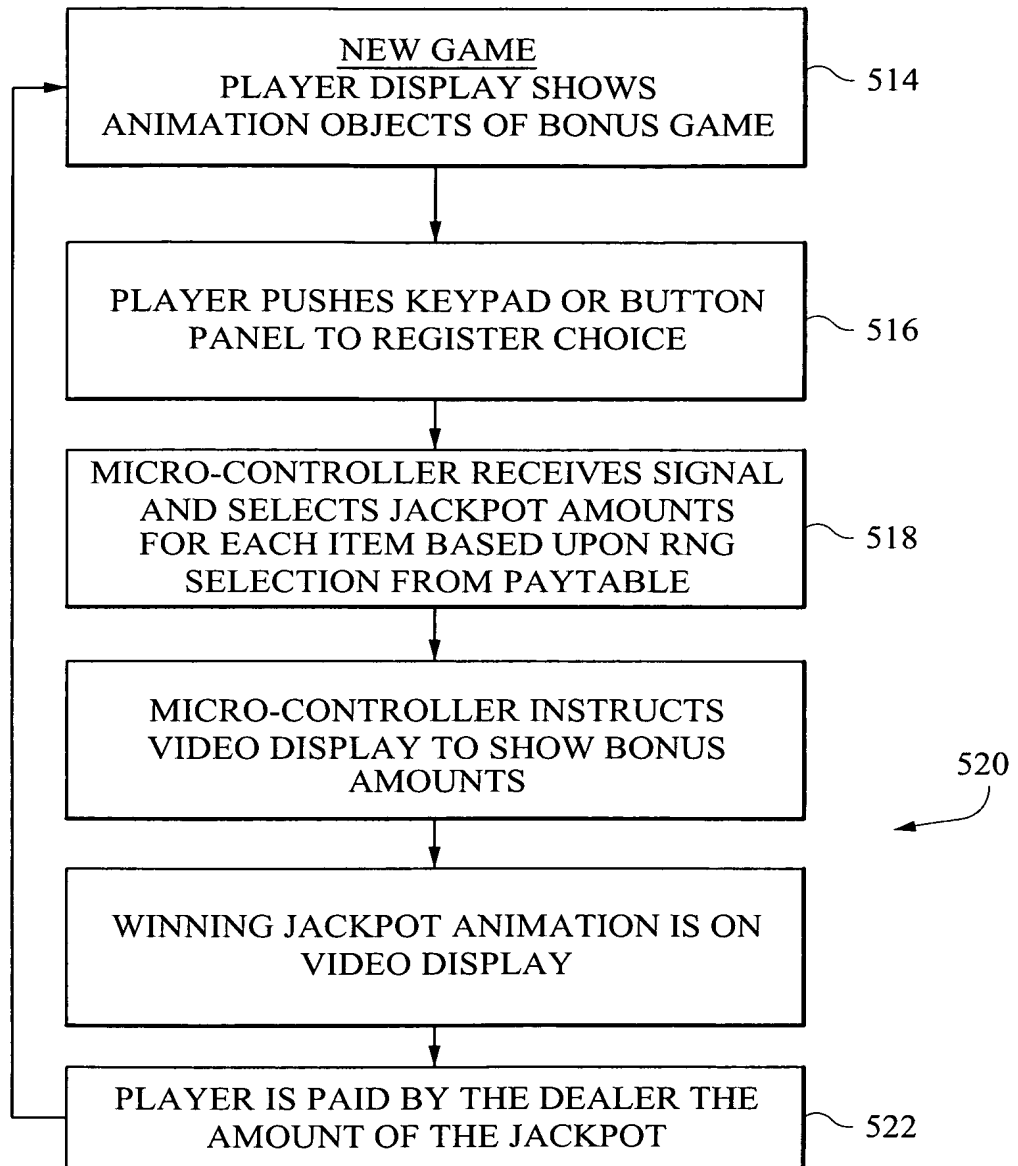


Fig. 9

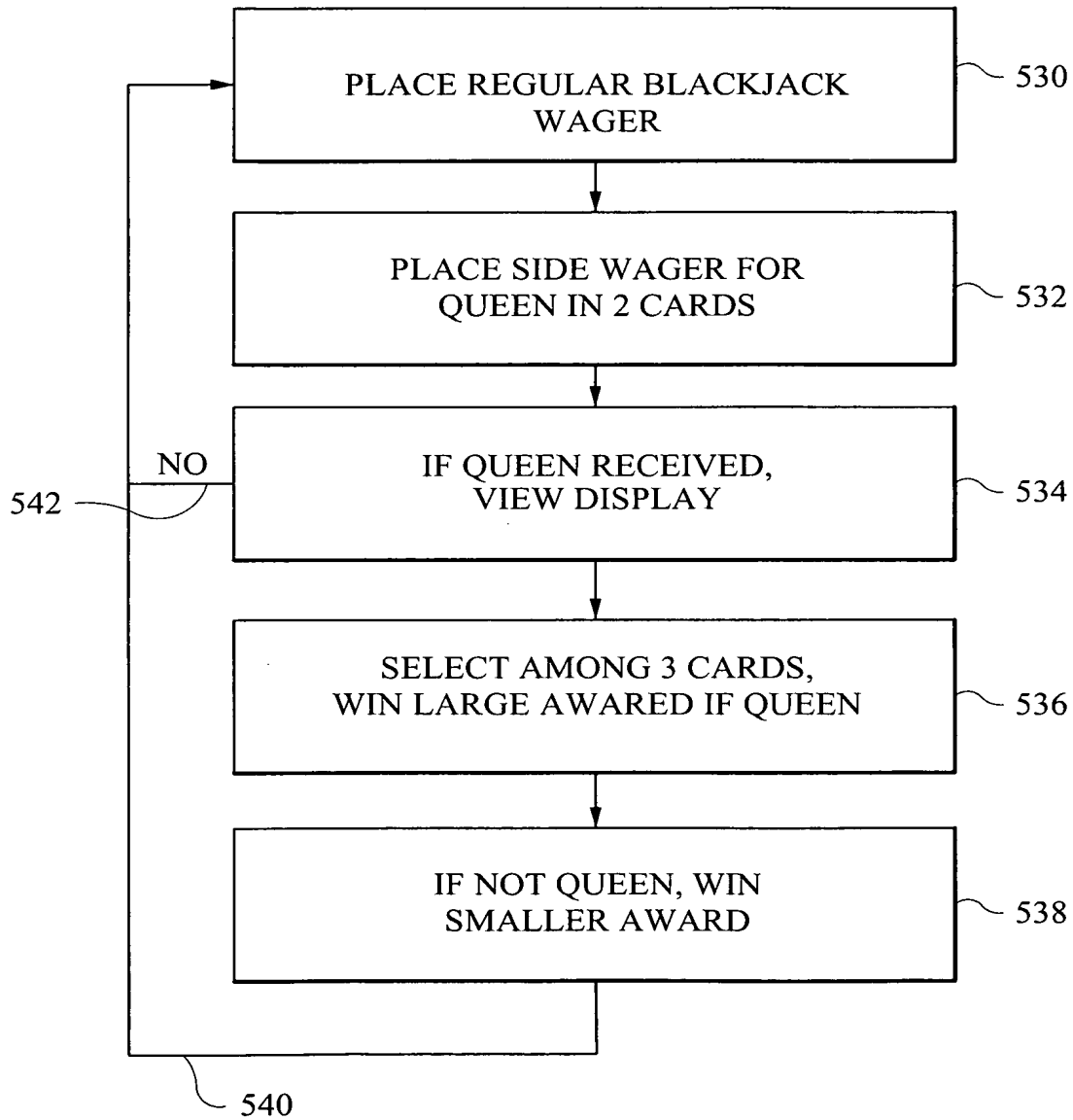


Fig. 10

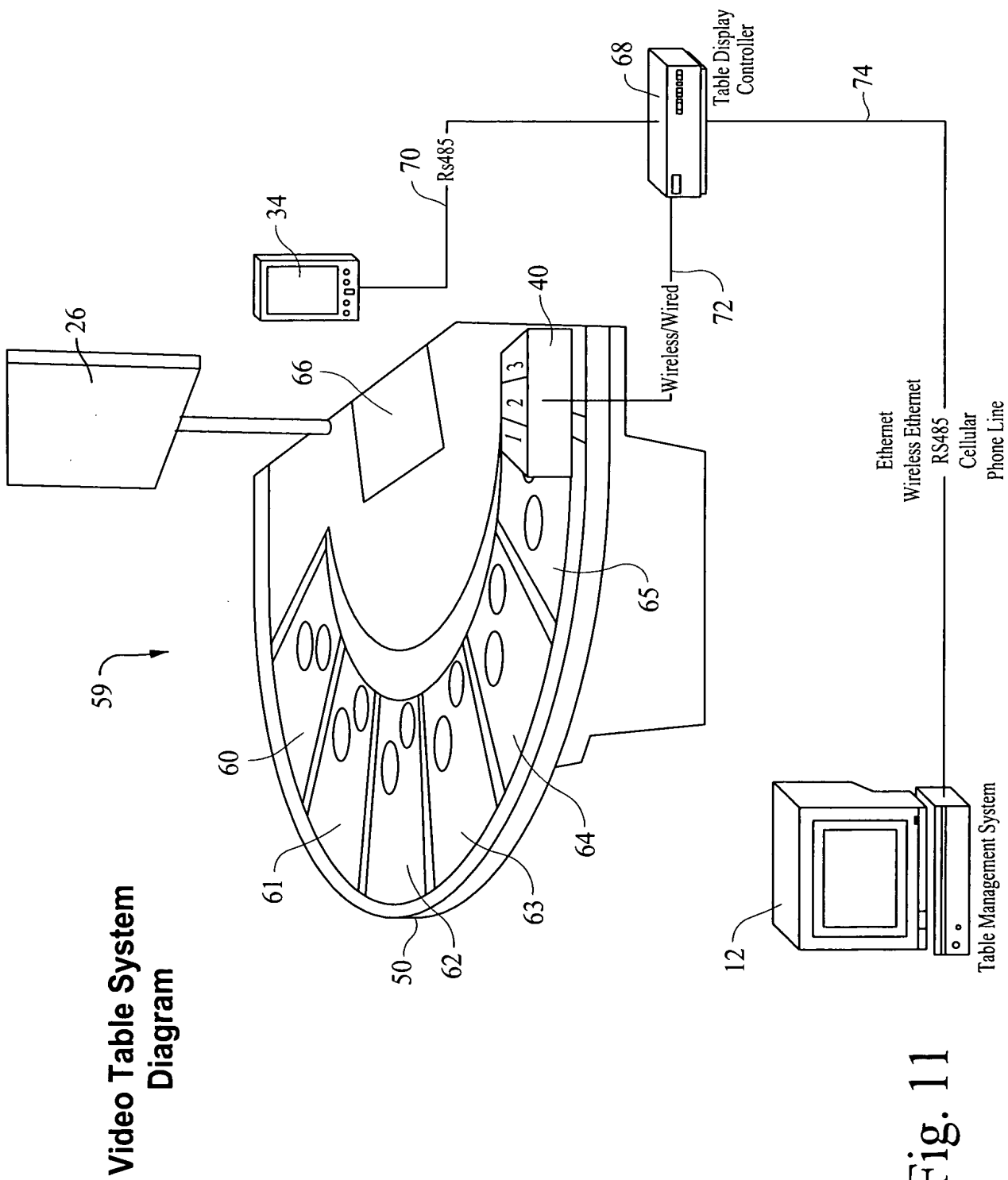


Fig. 11

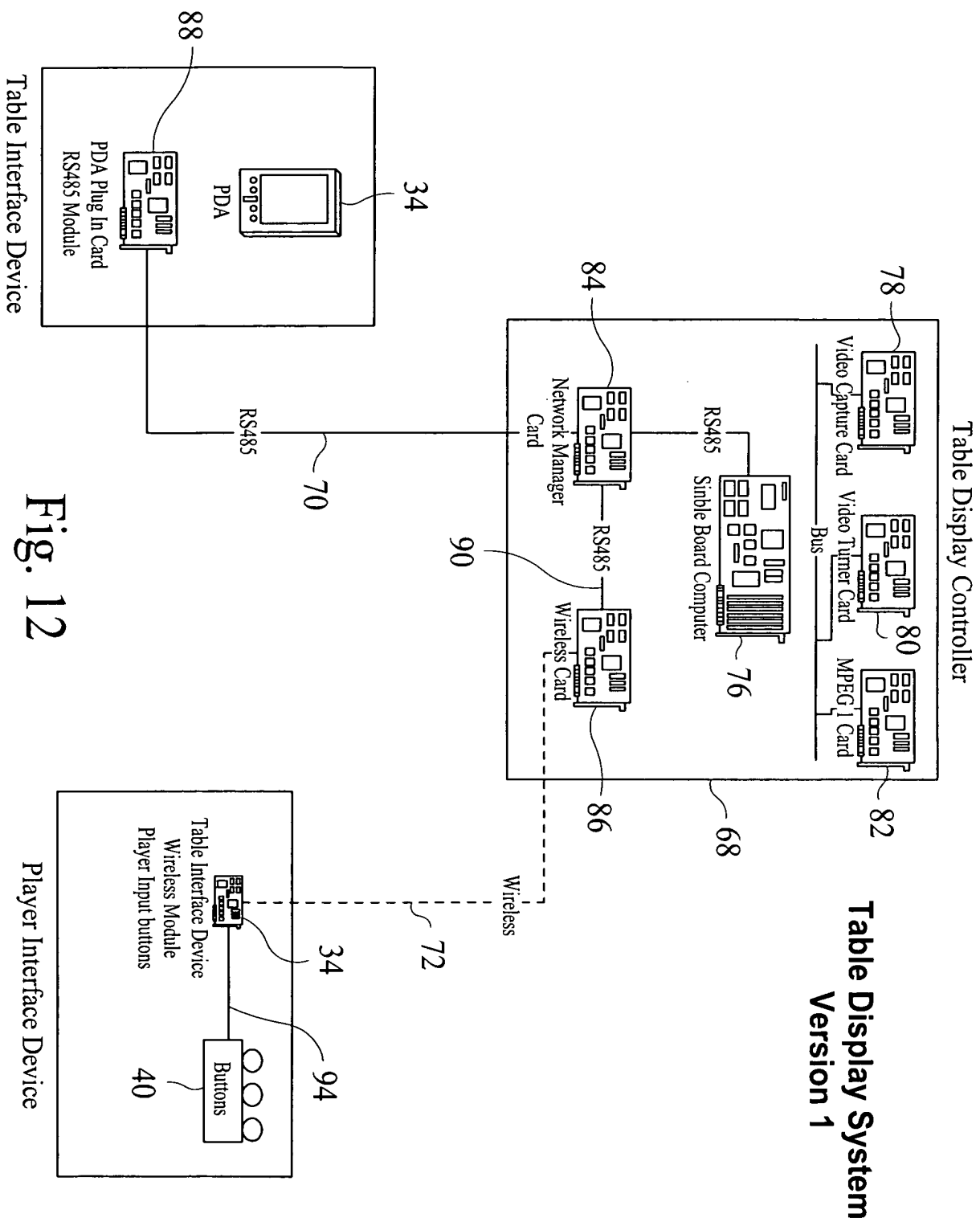


Fig. 12

**Table Display System
 Version 2**

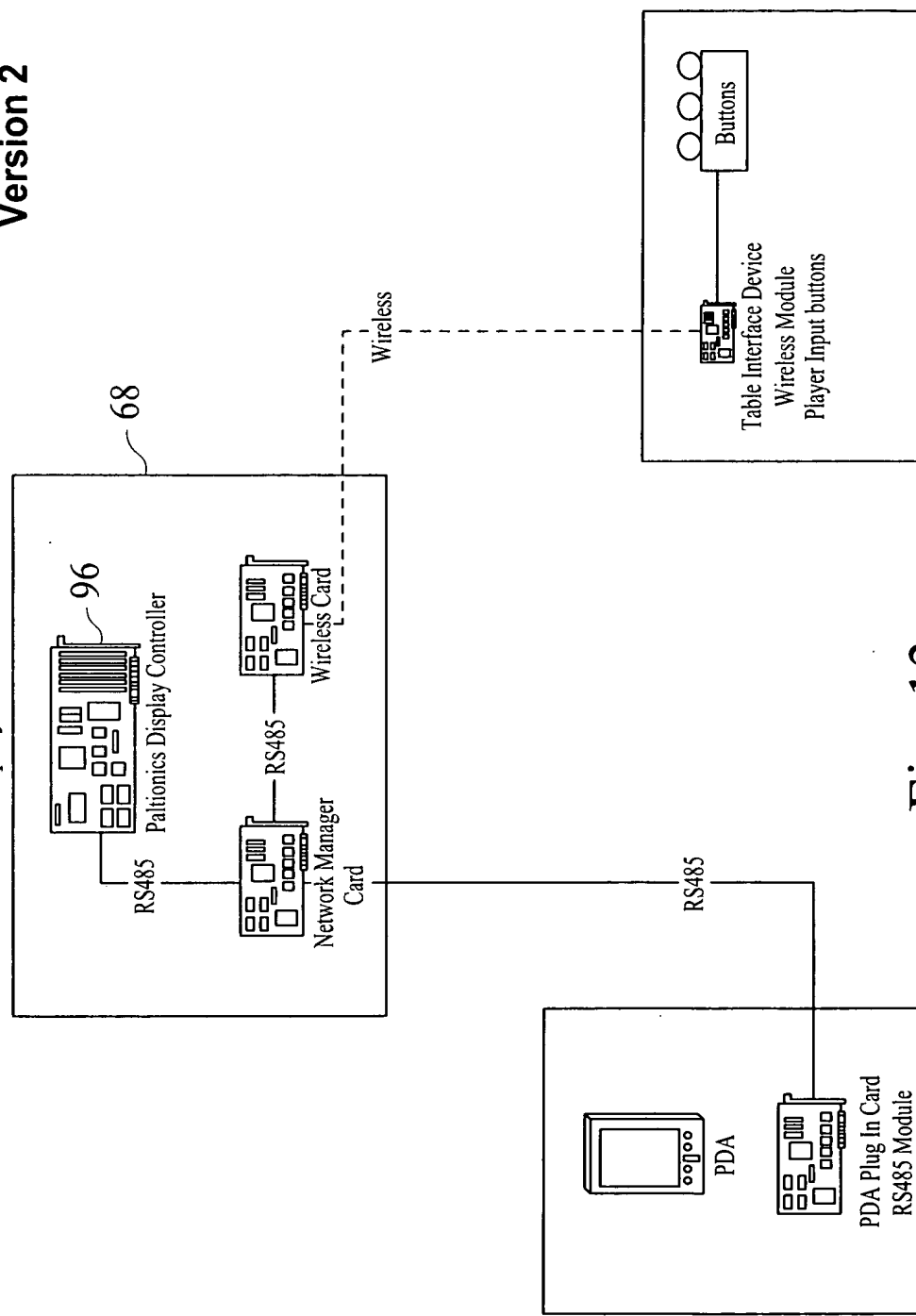


Fig. 13

Player Interface Device

Table Interface Device

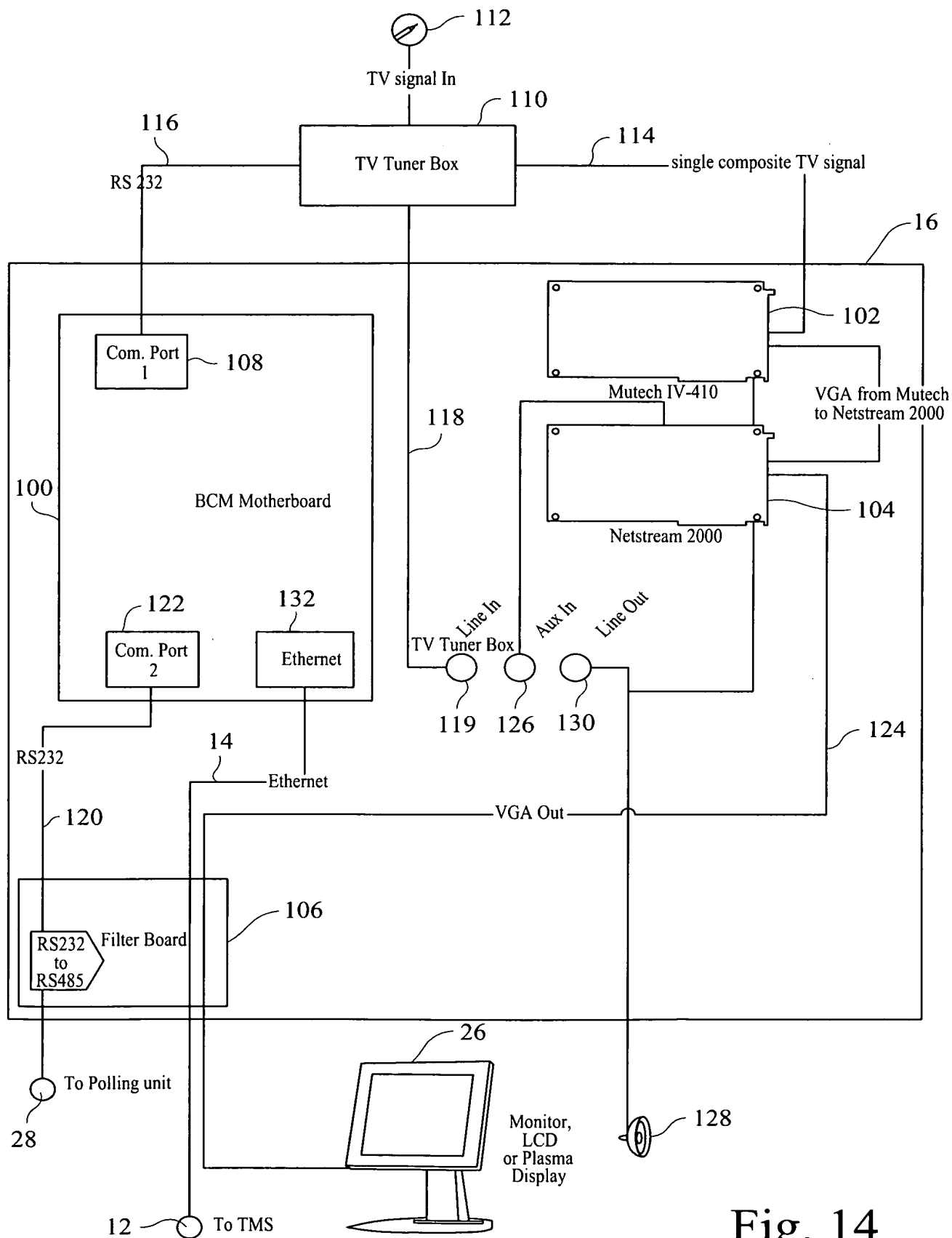


Fig. 14

Table Display System

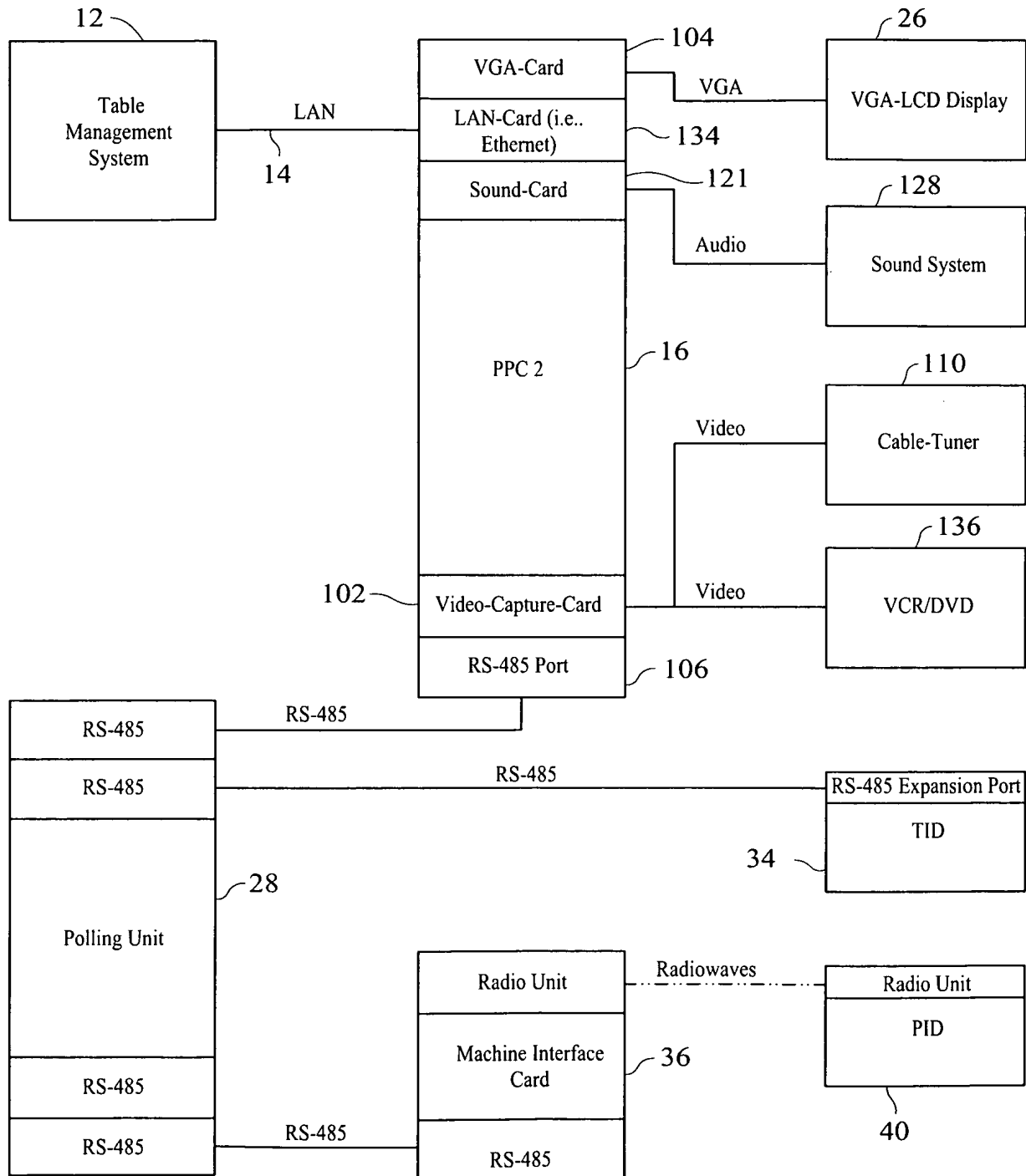


Fig. 15

Table Input Device General Initialization

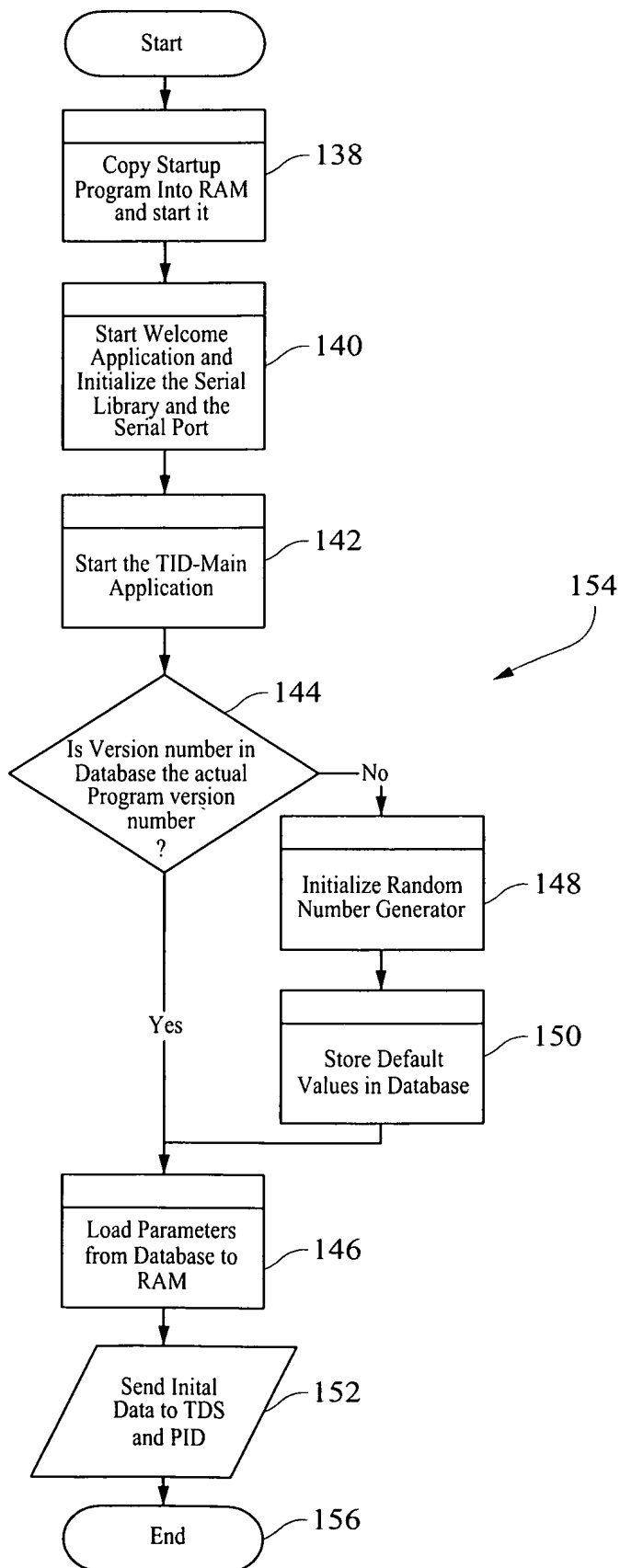


Fig. 16

Table Input Device User Interface

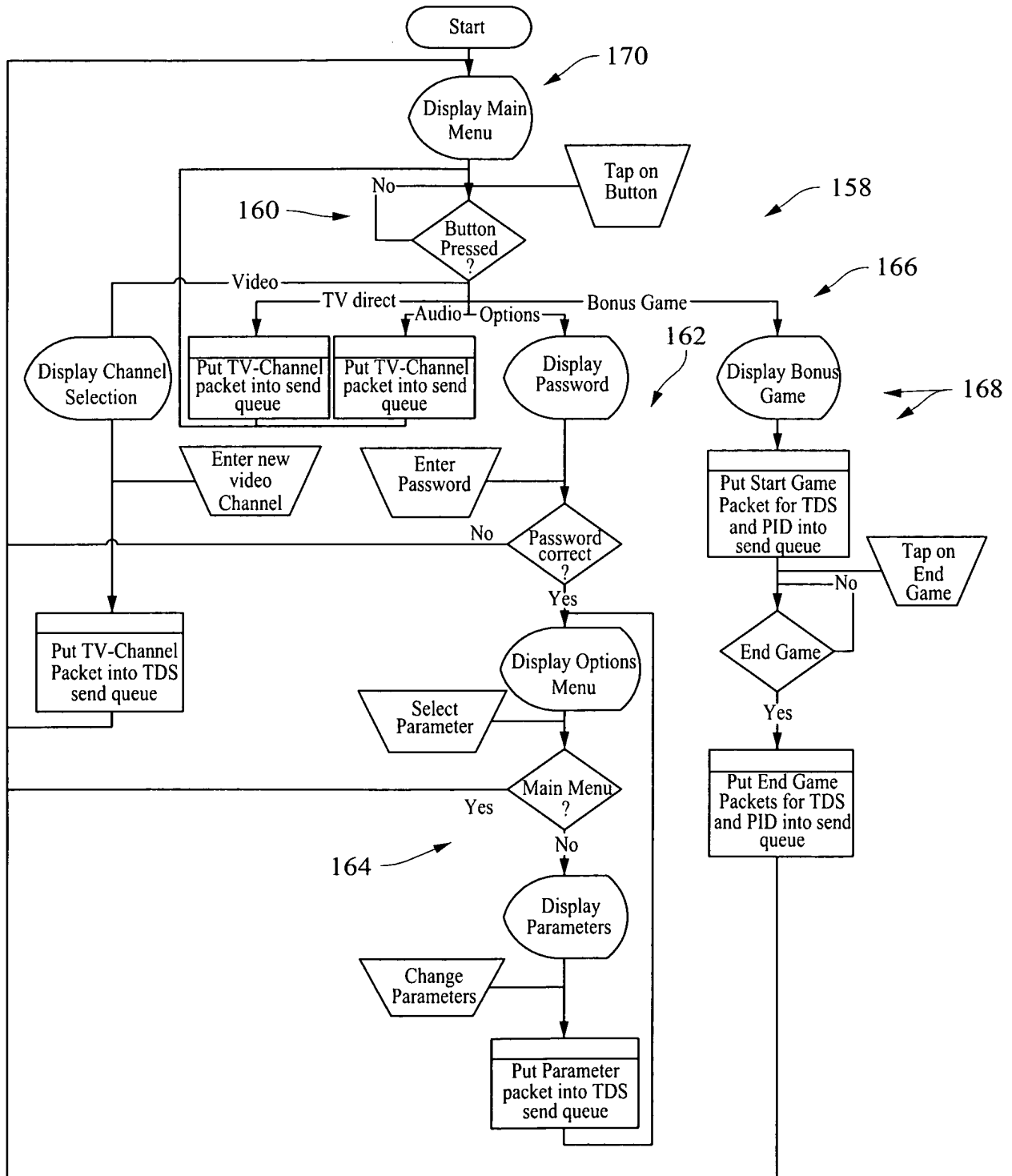


Fig. 17

Table Input Device Serial Communication

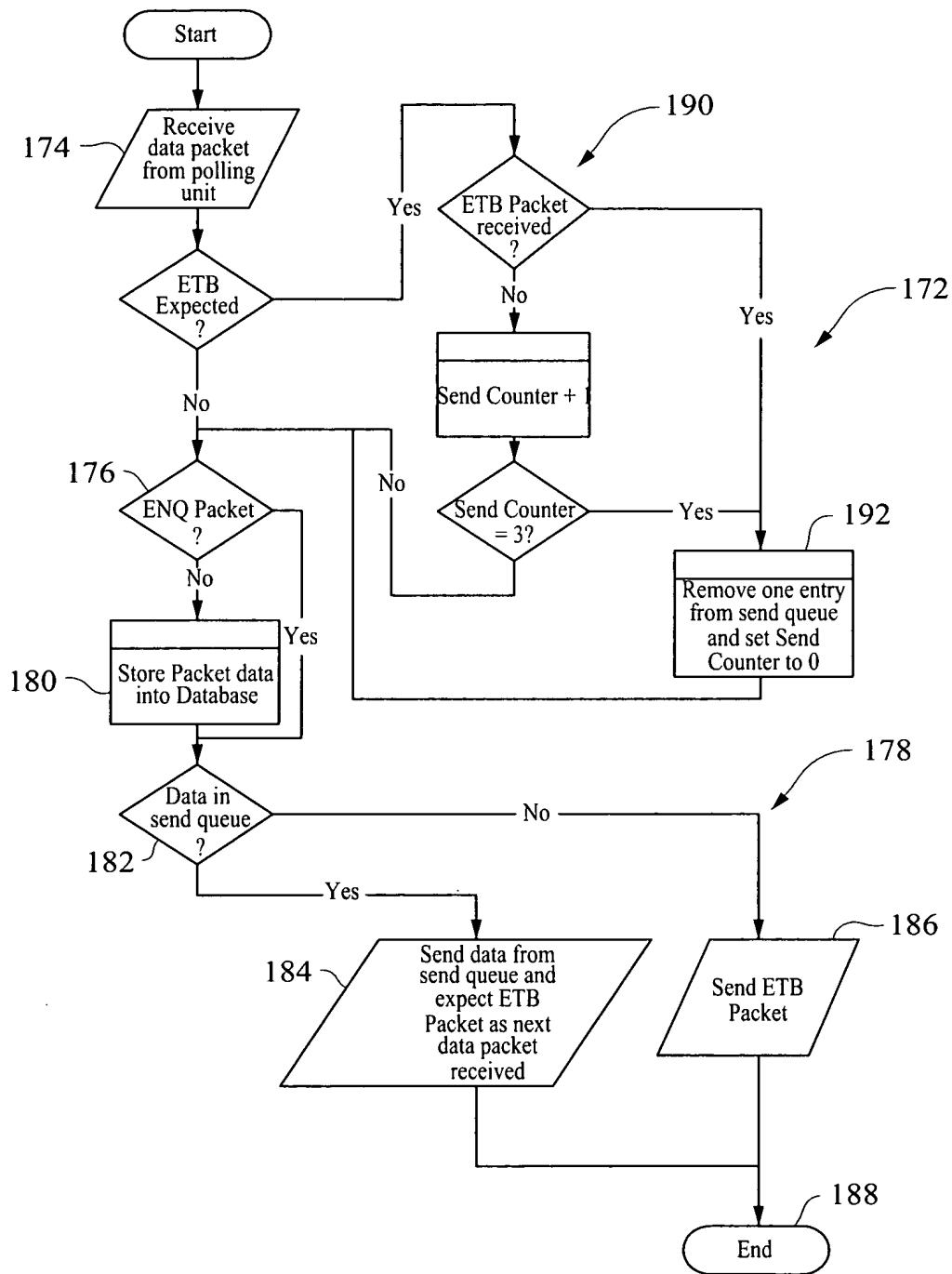


Fig. 18

Flow Chart Wireless Communications Module

Flow Chart Player Input Device

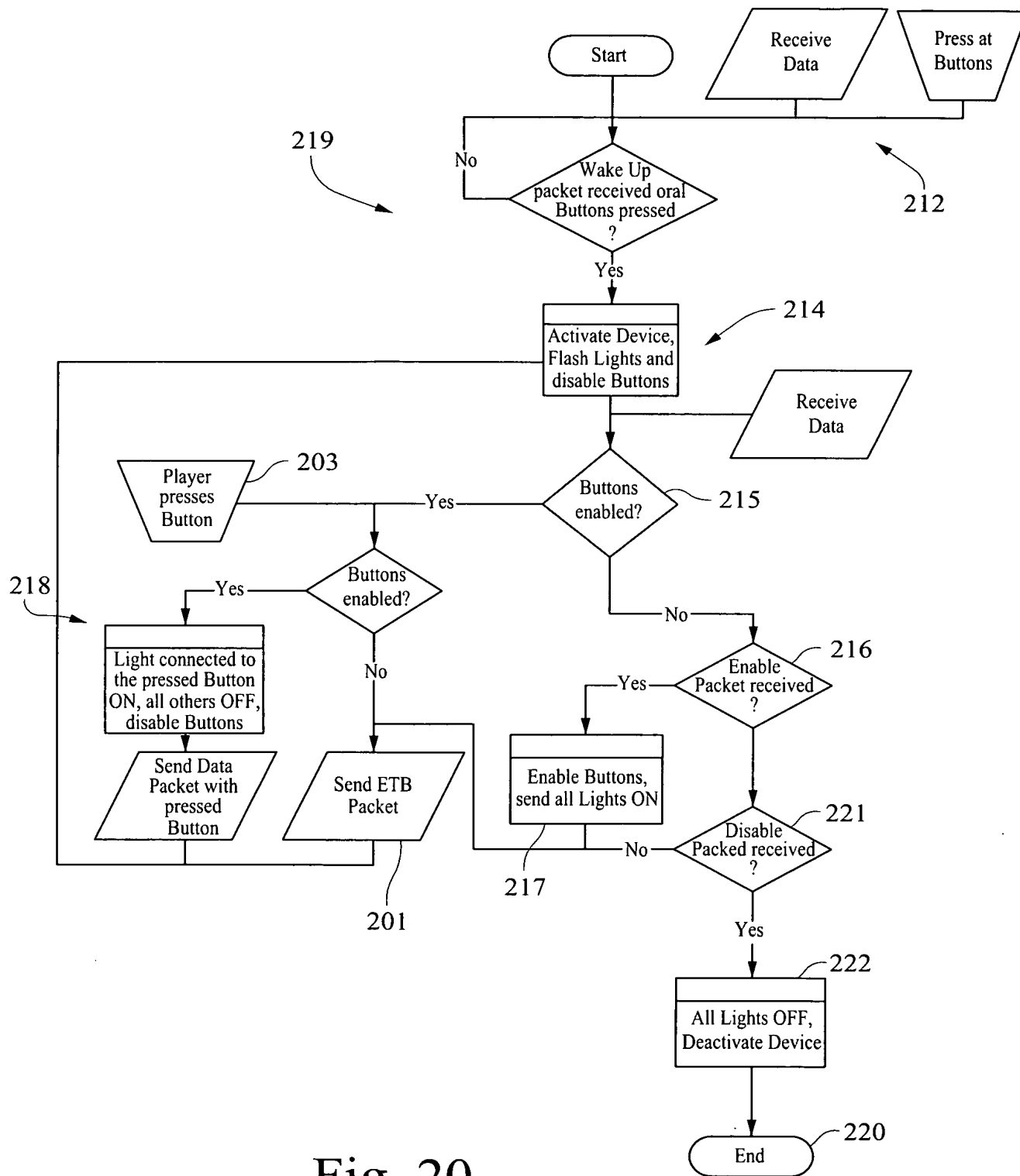


Fig. 20

Flow Chart Polling Unit

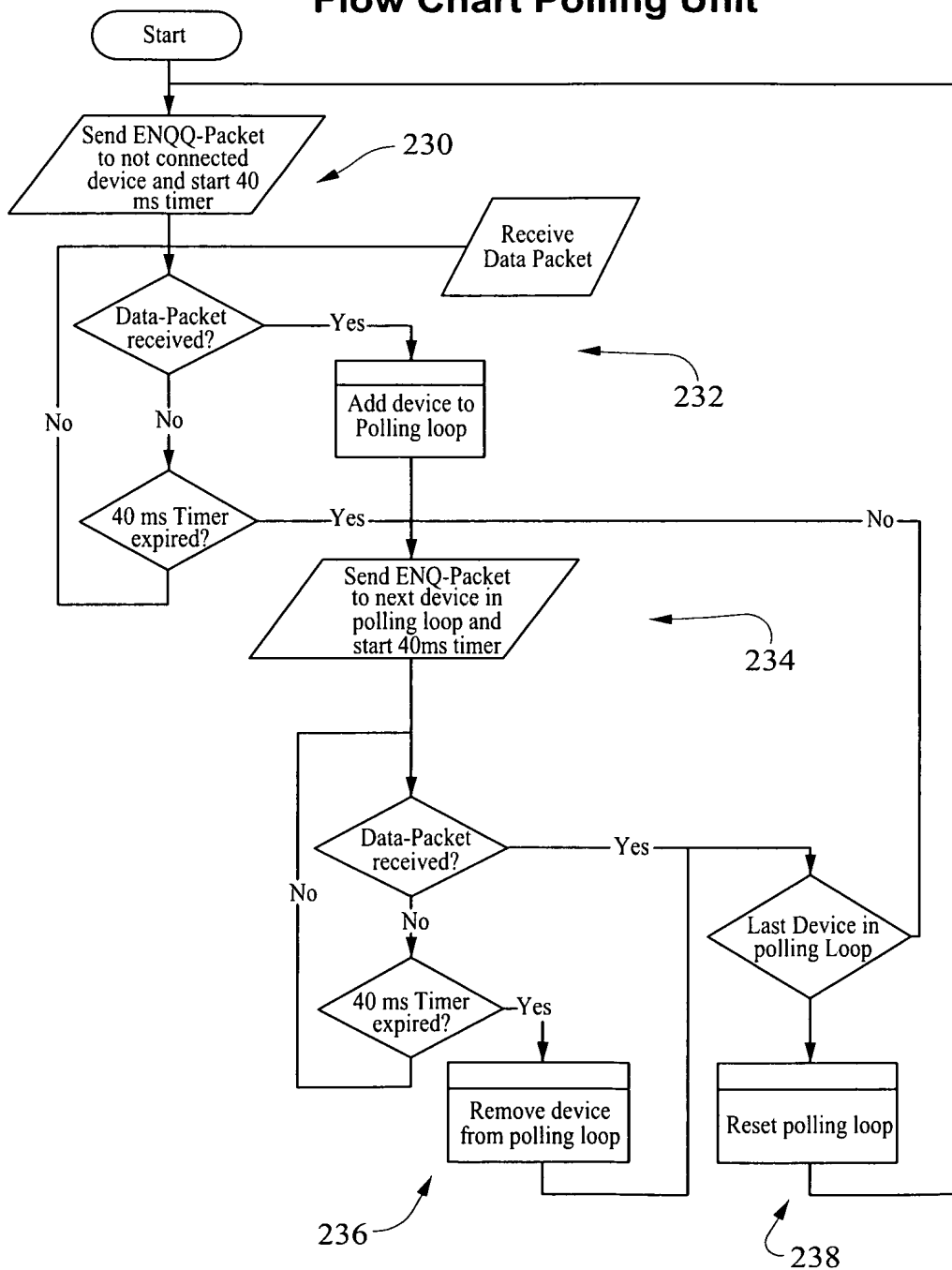


Fig. 21

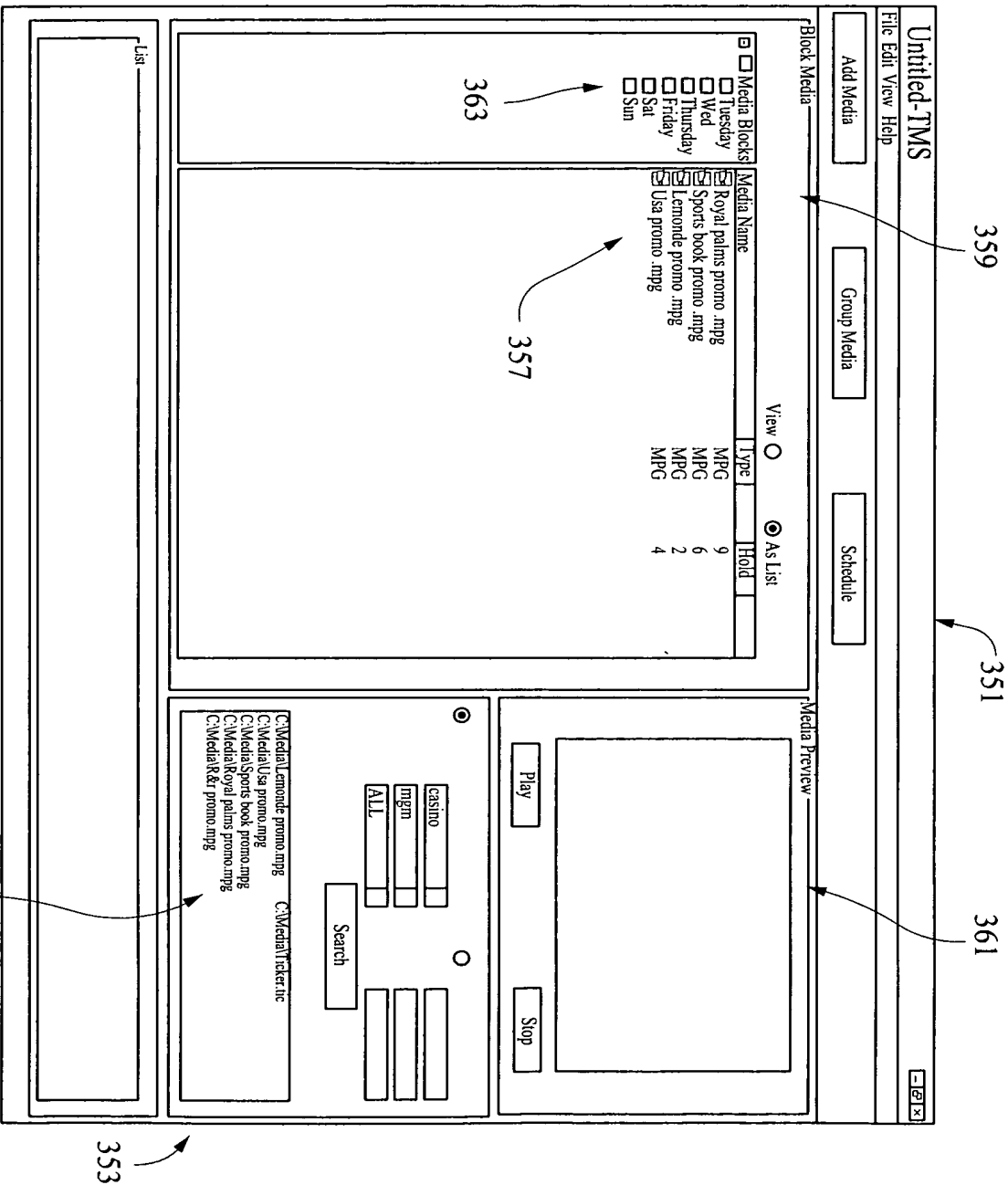


Fig. 23

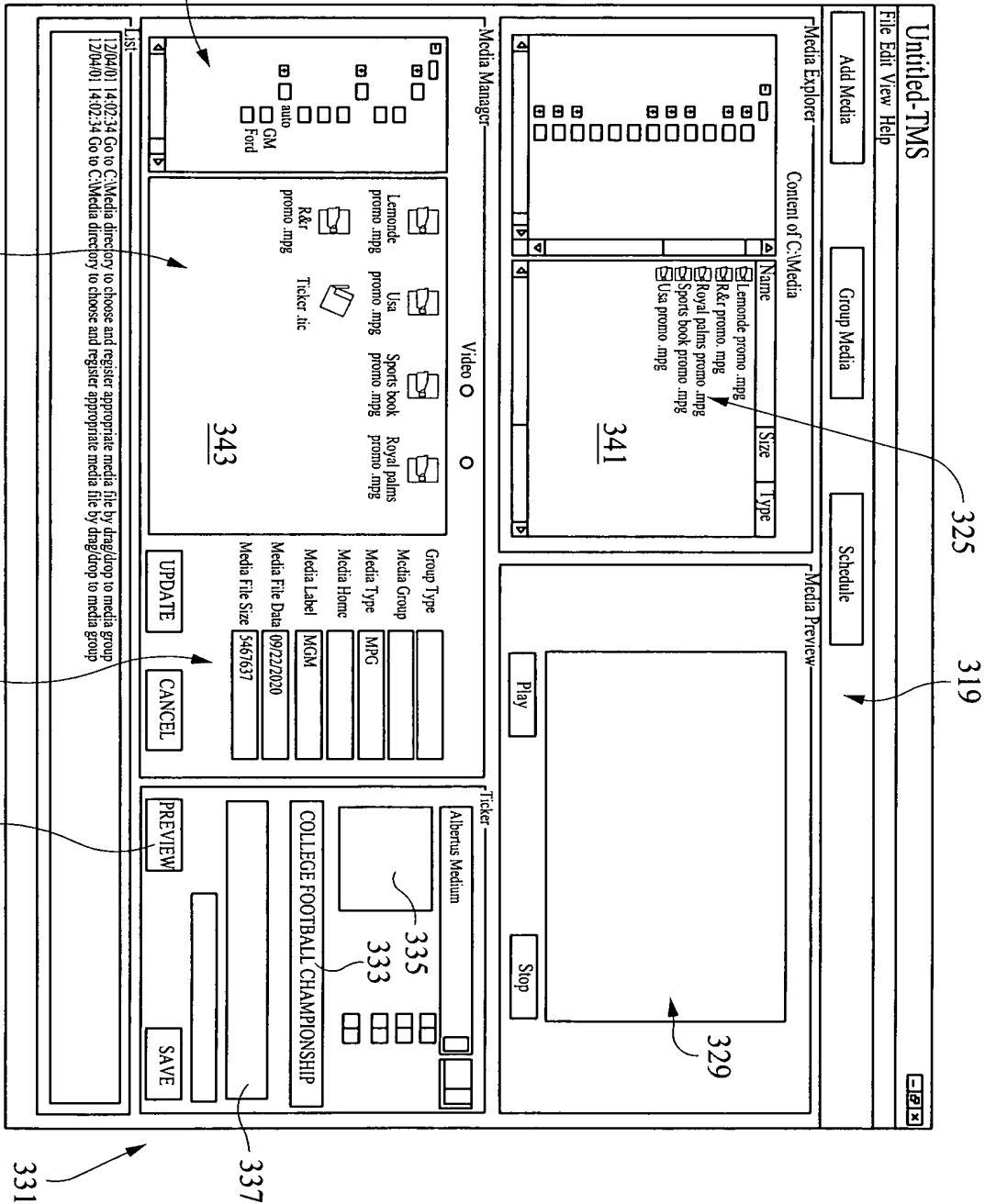


Fig. 24

Fig. 25A

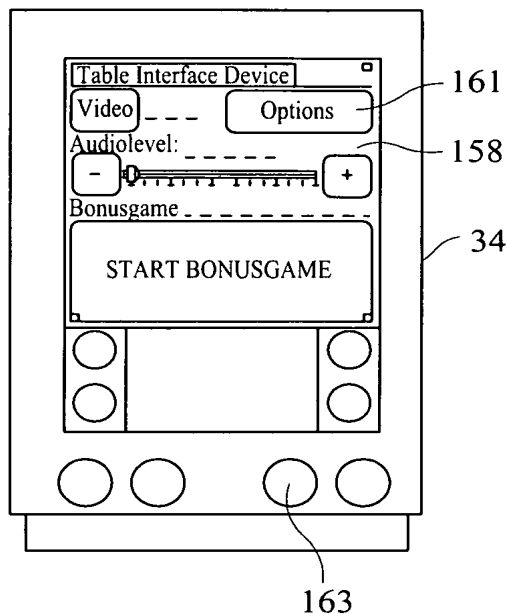


Fig. 25B

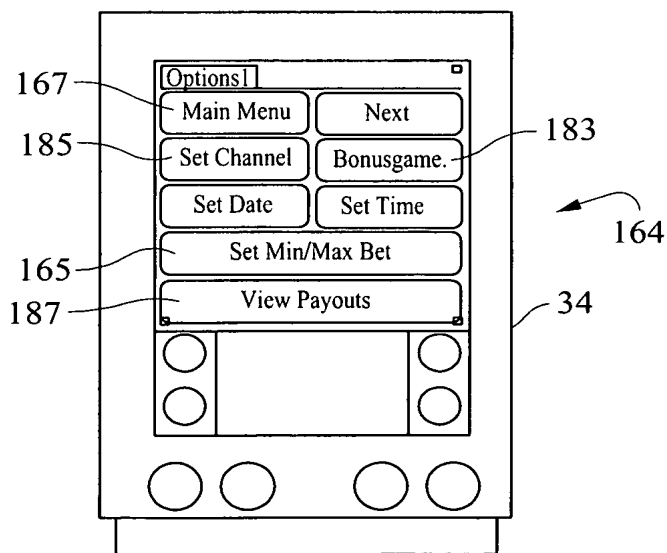
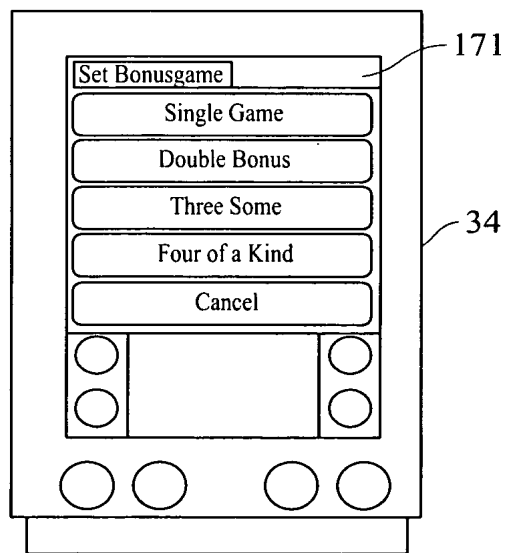
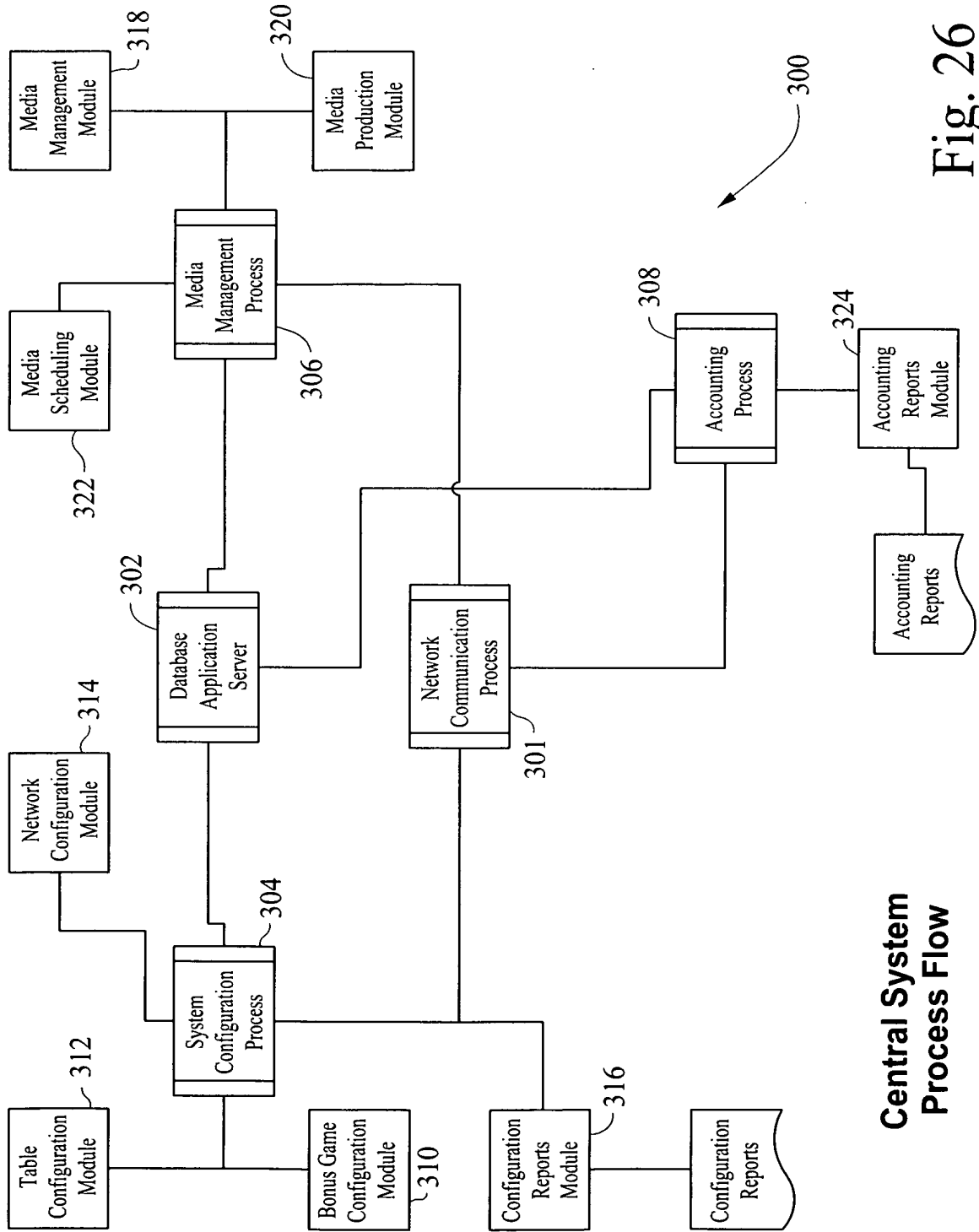


Fig. 25C





**Central System
 Process Flow**

Fig. 26